

Amendments to the Drawings

Attached are replacement drawing sheets for amended Figures 1, 2, 3 and 6-10.

Under cover of the replacement drawings, the Applicant proposes making the below-stated changes to the drawings as filed.

Figures 1-3:

Add to each of these Figures the legend "(PRIOR ART)" to indicate that, as set forth in the specification these Figures illustrate background prior art illustrations of bilinear interpolation, trilinear interpolation and mip-maps. (Specification, page 3, line 20 to page 4, line 7).

Figure 6A and Figure 6B (Previously Figure 6):

Figure 6 is now relabeled so that the top tile arrangement is Figure 6A and the bottom tile arrangement is Figure 6B.

Figure 7:

Partially amend the legend associated with the lower decode unit 70 to the trilinear interpolator 64 from "upper" to "lower". This change is made to make it clear that it is the lower decode unit 70 that provides the low level bits for the trilinear interpolation. (Specification, page 18, line 30 to page 19, line 16).

Figure 8A and Figure 8B (Previously Figure 8):

Figure 8 is now relabeled so that the top tile arrangement is Figure 8A and the bottom tile arrangement is Figure 8B.

Figures 9 and 10:

Partially amend the legend associated with the lower arrow extending from the DU deallocator 84 to the trilinear

interpolator 64 from "upper" to "lower". This change is intended to make it clear that the DU deallocator 84 supplies both the four upper and four lower texels to the trilinear interpolator. (Specification, page 20, line 33 to page 29, line 3).



1/9

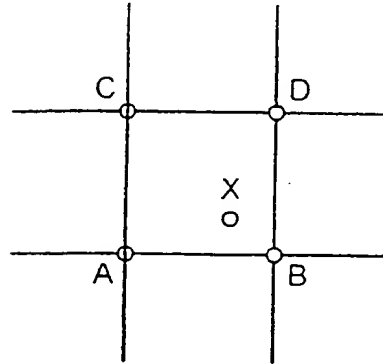


FIG. 1 (PRIOR ART)

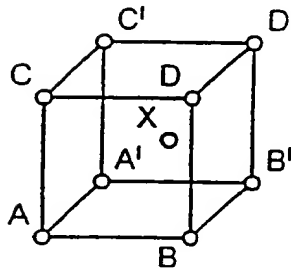


FIG. 2 (PRIOR ART)

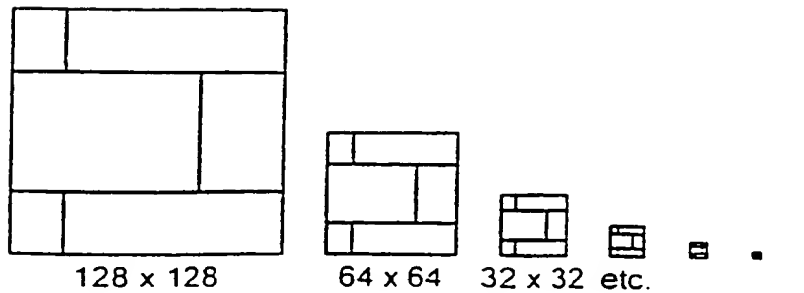


FIG. 3 (PRIOR ART)

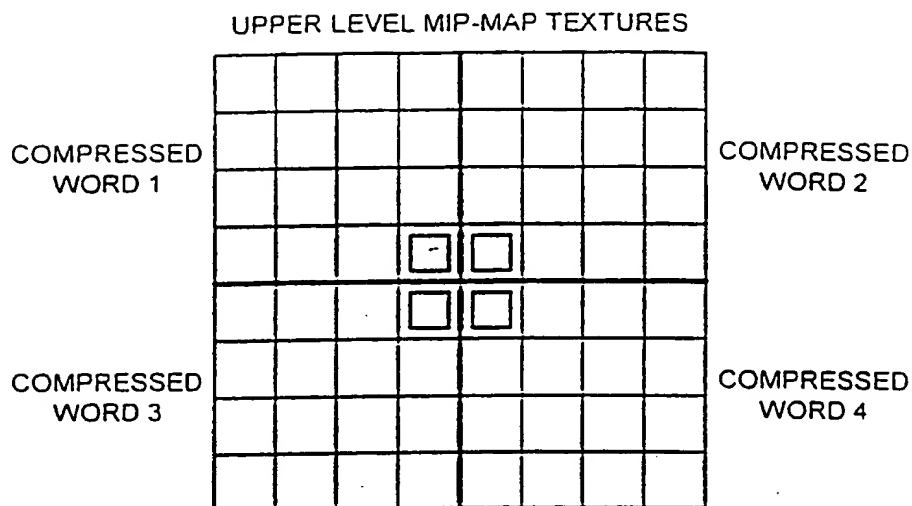


FIG. 6A

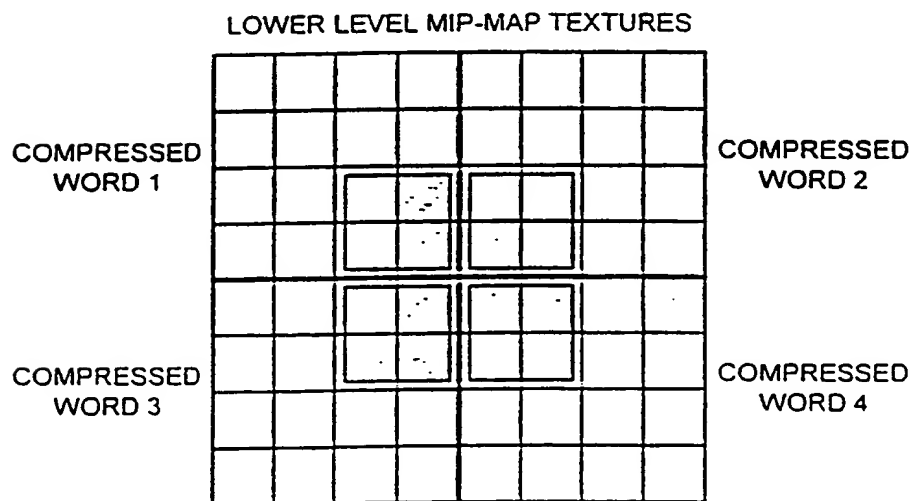
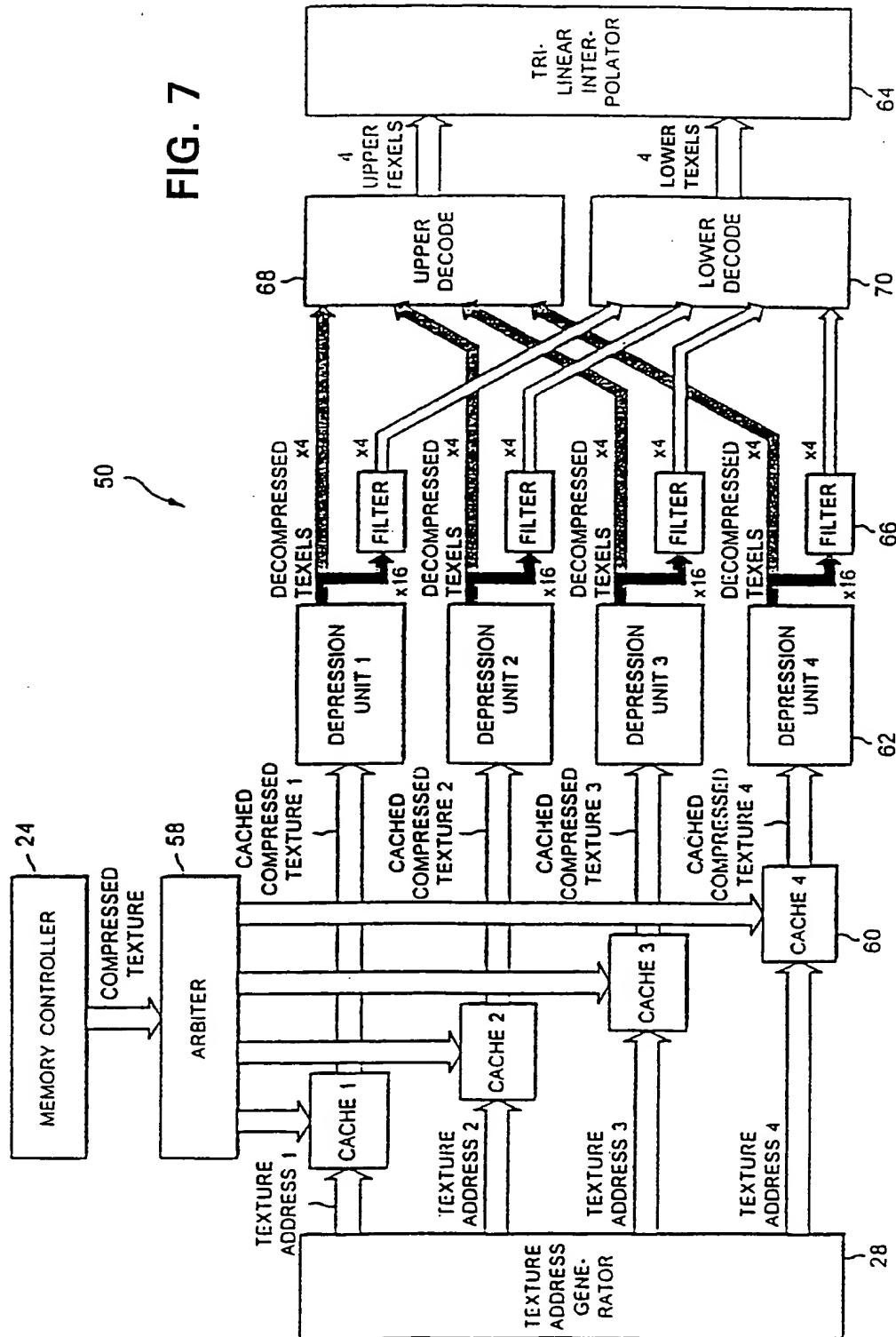


FIG. 6B

5/9



FIG. 7





6/9

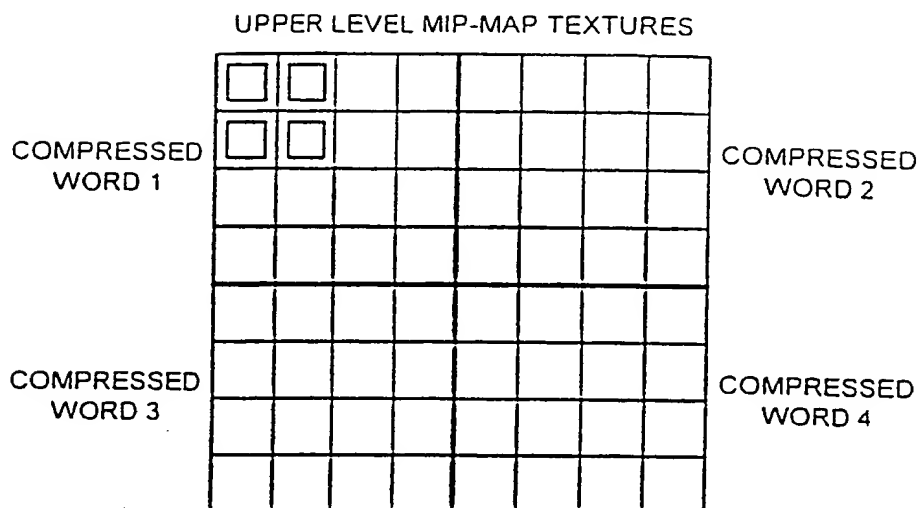


FIG. 8A

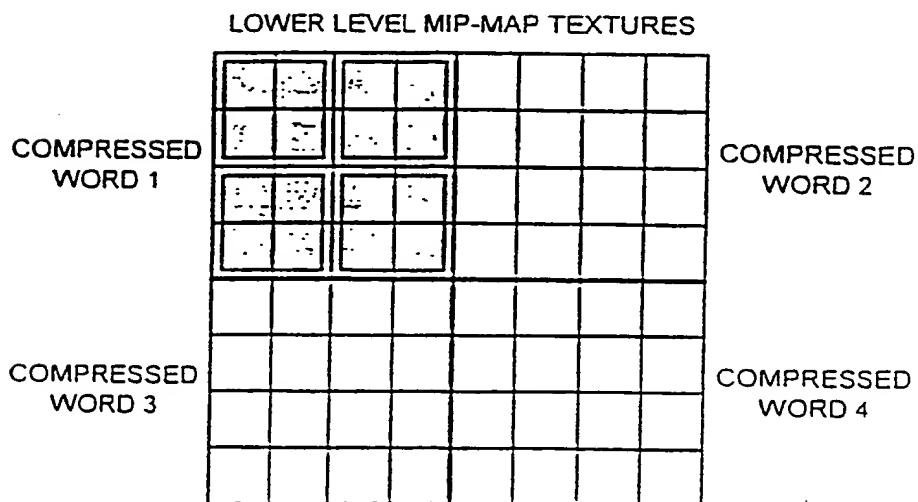
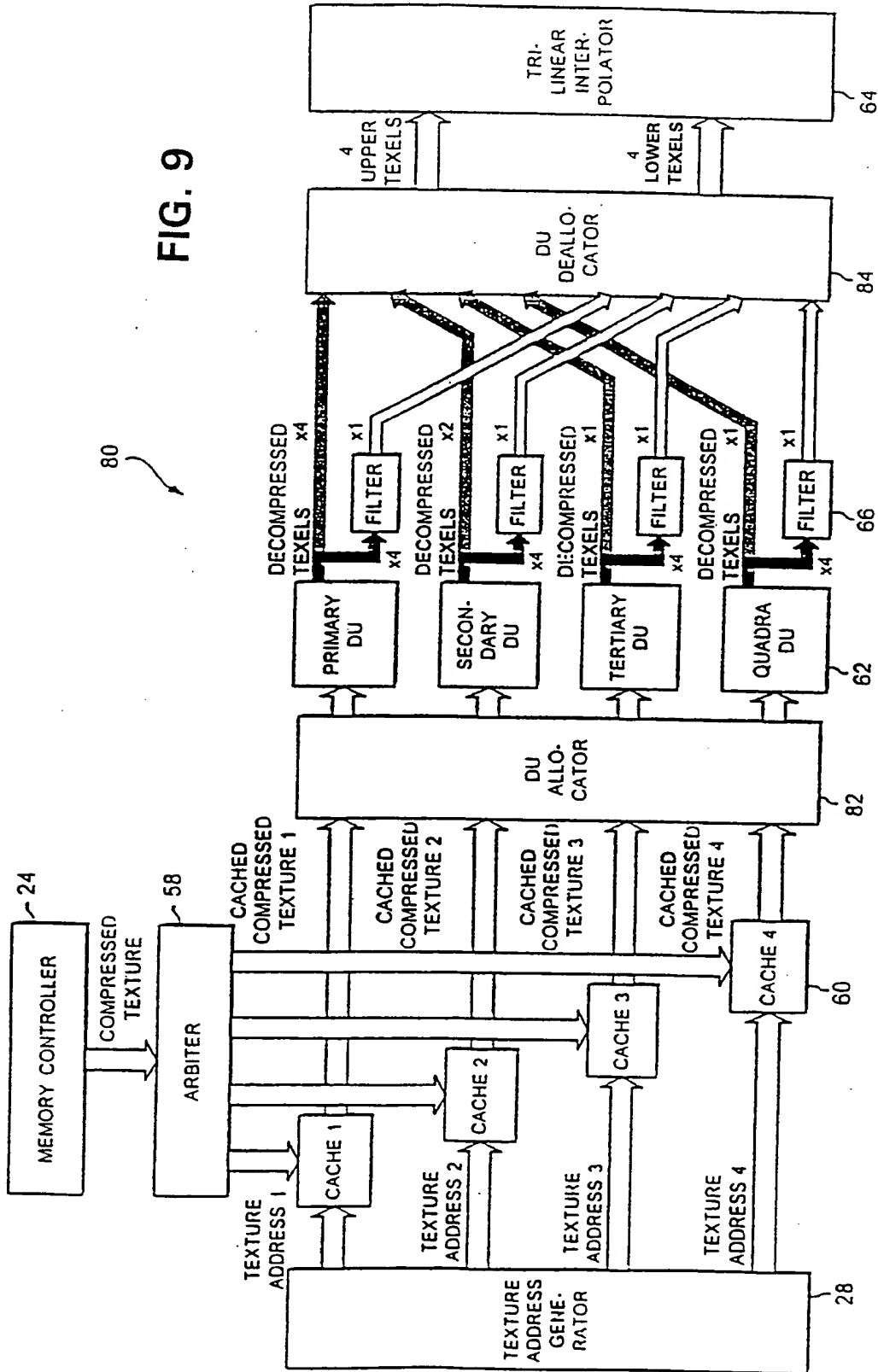


FIG. 8B



7/9

FIG. 9



8/9

FIG. 10

